

Tagger Microscope LV&Bias Distribution

Specs total of 6 backplanes/chassis:

Bias: +100V @ 500mA
 Preamp VCC: +6V @ 8A
 CMOS VCC: +5V @ 3A
 DAC: -5V @ 65mA

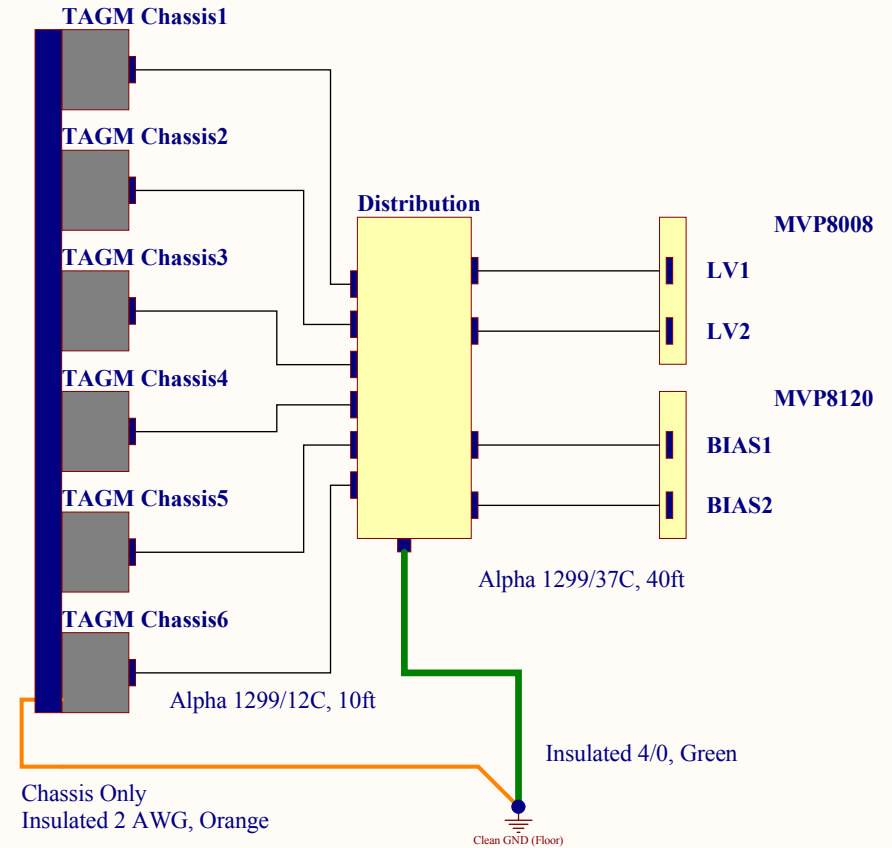
Power Supply Configuration for 6 backplane/chassis:

a) MPV8120 (use 6 of 8 channels)
 +100V [120V, 100mA] - 1ch/backplane (83mA) - Sense at each backplane

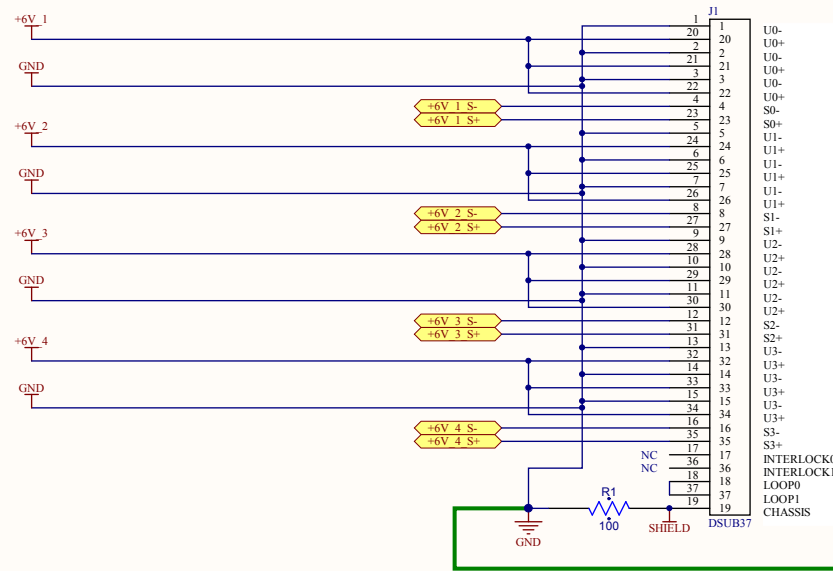
b) MPV8008 (use 8 of 8 channels)
 +6V [8V, 5A] - 1ch/backplane (1.33A) - Sense at each backplane
 +5V [8V, 5A] - 1ch/all backplanes (3A) - Sense at distribution box
 -5V [8V, 5A] - 1ch/all backplanes (65mA) - Sense at distribution box

Cabling:

- a) Supply Modules to distribution box: Qty. 4 cables
 Alpha 1299/37C - 37 conductor, shielded, 22AWG, CL2, 40ft long
 D-Sub 37 Plug (@supply) & D-sub 37 Receptacle (@Dist Box)
 Connect shield to pin 19, both sides
- b) Distribution box to chassis: Qty. 6 cables
 Alpha 1299/12C - 12 conductor, shielded, 22AWG, CL2, 10ft long
 D-Sub 15 Plug (@Dist Box) & Amphenol (@Chassis)
 Connect shield to pin 11, D-sub side only
- c) TAGM Chassis Grounding: Qty. 1
 2 AWG, Insulated, Orange
- d) Distribution Box Grounding Stud: Qty. 1
 Use one Blue Sea 2201-2202 Feed Thru Stud in Dist Box
 To Connect PCB to Clean GND with 4/0 cable



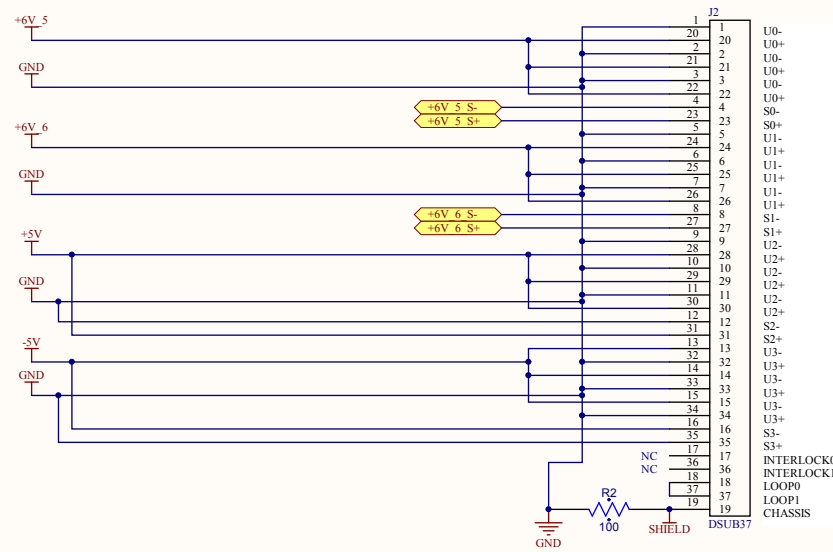
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Drawn FJB	Date: 3/28/2014	Title TAGM LV&BIAS Distribution Notes & Diagram	
Checked	Date:	Size: B	Revision: 1
Approved FJB	Engineer FJ Barbosa	File: C:\Users\barbosa\ALTUM\TAGGER\LV&BIAS	Sheet of * Distribution/Tagger LV&Bias



LV1 Input

D-SUB37 Plug from MPOD MPV8008LI (1 of 2)

Blue Sea 2201-2202 Feedthrough Stud
(connect to Clean GND pad on floor with 4/0 insulated cable)



LV2 Input

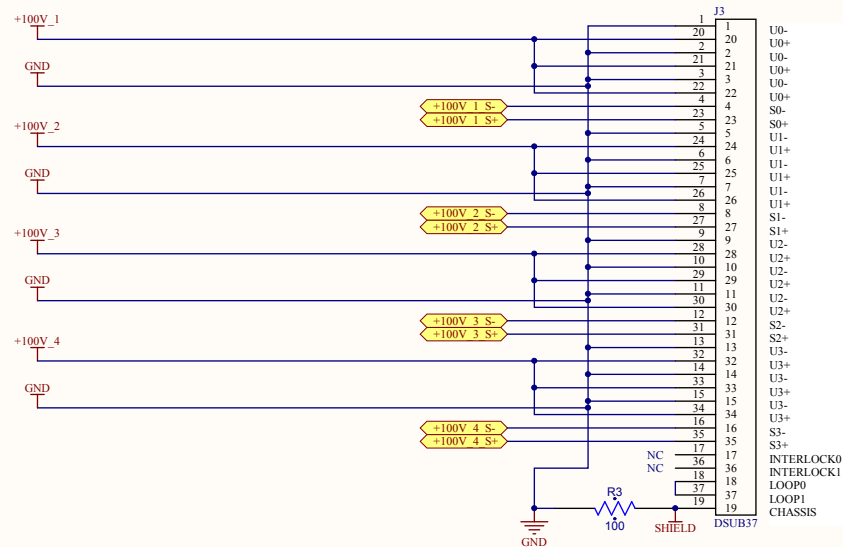
D-SUB37 Plug from MPOD MPV8008LI (2 of 2)

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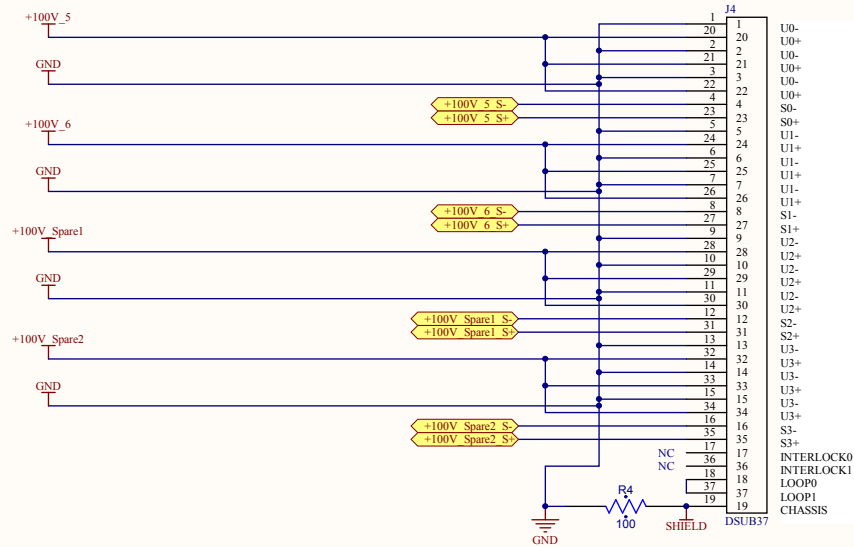


Drawn FJB	Date: 3/28/2014	Title TAGM LV&BIAS Distribution LV Inputs to Distribution Chassis
Checked FJB	Date:	
Approved FJB	Size: B	Drawing Number: 2
Engineer FJ Barbosa	File: C:\Users\barbosa\ALTUM\TAGGER\LV&BIAS	Sheet of * Distribution\Tagger LV&Bias Dis



BIAS1 Input

D-SUB37 Plug from MPOD MPV8120I (1 of 2)

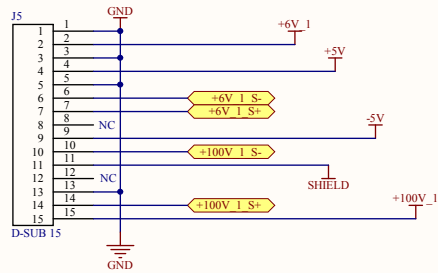


BIAS2 Input

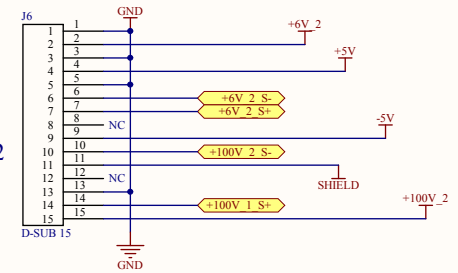
D-SUB37 Plug from MPOD MPV8120I (2 of 2)

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Drawn FJB	Date: 3/28/2014	Title TAGM LV&BIAS Distribution BIAS Input to Distribution Chassis	
Checked	Date:	Approved FJB	Revision: 3
Engineer FJ Barbosa	File: C:\Users\barbosa\ALTUM\TAGGER\LV&BIAS	Sheet of * Distribution Tagger LV&Bias Dis	

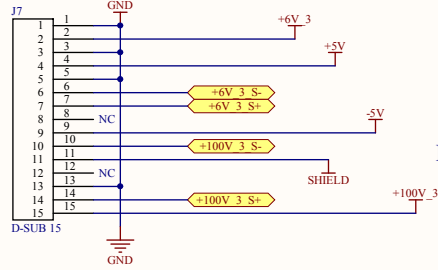
OUT1
D-SUB15 Receptacle to TAGM Chassis 1



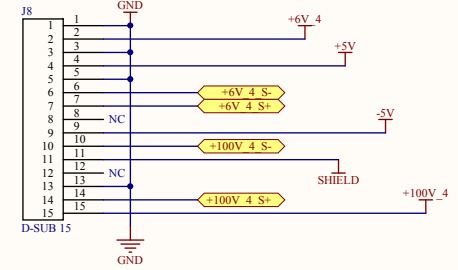
OUT2
D-SUB15 Receptacle to TAGM Chassis 2



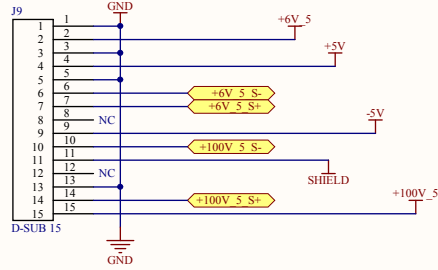
OUT3
D-SUB15 Receptacle to TAGM Chassis 3



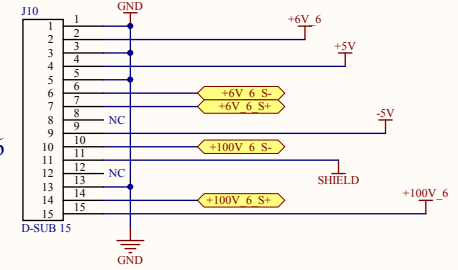
OUT4
D-SUB15 Receptacle to TAGM Chassis 4



OUT5
D-SUB15 Receptacle to TAGM Chassis 5



OUT6
D-SUB15 Receptacle to TAGM Chassis 6



PINOUT

1 - GND	6 - +6V_Sense-	11 - Shield
2 - +6V	7 - +6V_Sense+	12 - NC
3 - GND	8 - NC	13 - GND
4 - +5V	9 - -5V	14 - +100V_Sense+
5 - GND	10 - +100V_Sense-	15 - +100V

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Drawn FJB	Date: 3/28/2014	Title TAGM LV&BIAS Distribution LV&BIAS Outputs to TAGM Chassis (6)	
Checked	Date:	Size: B	Revision: 4
Approved FJB	Engineer FJ Barbosa	File: C:\Users\barbosa\ALTUM\TAGGER\LV&BIAS	Sheet of * Distribution\Tagger LV&Bias